Rec'd PCT/PTO 2 7 MAY 2005.





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INTERNATIONAL PRELIMINARY EXAMINATION REPORT

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rslati	PC	\mathbf{T}	10/	536654
INTERNATI	IONAL PRELIMINA	RY EXAMIN	_	
·	(PCT Article 36	and Rule 70)		
Applicant's or agent's file reference S2819 GC/lko	FOR FURTHER ACT			ansmittal of Internat eport (Form PCT/IPEA/
International application No. PCT/EP2003/012350	International filing date (05 November 2003	•	1	(day/month/year) aber 2002 (27.11.20
International Patent Classification (IPC) or r H04L 7/04	<u> </u>		1	
Applicant	INFINEON TECHN	OLOGIES AG		
2. This REPORT consists of a total of This report is also accompa been amended and are the been amended and Section These annexes consist of a	anied by ANNEXES, i.e., sh basis for this report and/or s n 607 of the Administrative	eets of the descrip heets containing r Instructions under	tion, claims and ectifications ma	
3. This report contains indications rela	ating to the following items			10.00
I Basis of the repor	t			
II Priority				
	nt of opinion with regard to	novelty, inventive	step and industr	rial applicability
IV Lack of unity of in	nvention ent under Article 35(2) with lanations supporting such st	regard to novelty,	inventive step of	or industrial applicability
Contain de assurant		atement		
Comming deficate in	ts cited the international applicatio	n		
\ \frac{1}{2}	ons on the international app			
Date of submission of the demand	E	ate of completion	of this report	
24 June 2004 (24.06.2	2004)	16 Se	ptember 200	4 (16.09.2004)
Name and mailing address of the IPEA/EP	A	uthorized officer		
Facsimile No.	1	elephone No.		

Form PCT/IPEA/409 (cover sheet) (January 1994)



INTERNATIONAL PRELIMINARY EXAMINATION REPORT

In amount application No.
PCT/EP2003/012350

I. Basis of the report				
1. This report has been drawn on the basis of (Replacement sheets which have been furnished to the receiving Office in response to an invitation under Article 14 are referred to in this report as "originally filed" and are not annexed to the report since they do not contain amendments.):				
	the international	application as	s originally filed.	
	the description,	pages	1-9	_, as originally filed,
		pages		_, filed with the demand,
		pages		, filed with the letter of,
		pages		_, filed with the letter of
	the claims,	Nos	1-11	_ , as originally filed,
		Nos		, as amended under Article 19,
		Nos		_ , filed with the demand,
		Nos		, filed with the letter of,
		Nos		, filed with the letter of
\bowtie	the drawings,	sheets/fig _	1/2-2/2	_ , as originally filed,
		sheets/fig _		_, filed with the demand,
		sheets/fig _	· · · · · · · · · · · · · · · · · · ·	, filed with the letter of,
		sheets/fig _		, filed with the letter of
2. The amen	dments have result	ed in the canc	ellation of:	
	the description,	pages	· · · · · · · · · · · · · · · · · · ·	
	the claims,	Nos		
	the drawings,	sheets/fig _		
3. Thi	s report has been e	stablished as i	if (some of) the an	nendments had not been made, since they have been considered e Supplemental Box (Rule 70.2(c)).
	,	00010 00 11100	, 40 114144144 111 41	o supplemental Box (xtale 70/2(c)).
4. Additiona	l observations, if n	ecessary:		
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INTERNATIONAL PLIMINARY EXAMINATION REPORT

I	tional	application No.		
PCT	/EP	03/12350		

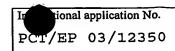
v.	Reasoned statement under Article 3 citations and explanations supporting	5(2) with regard to no	velty, inventive step or industrial applica	bility;
1.	Statement			
	Novelty (N)	Claims	1-11	YES
		Claims		NO
	Inventive step (IS)	Claims	1-11	YES
	,	Claims		NO
	Industrial applicability (IA)	Claims	1-11	YES
		Claims		NO NO

2. Citations and explanations

1. Reference is made to the following document:

D1: US-A-5 905 887 (CHEN HO-WEN ET AL) 18 May 1999 (1999-05-18).

- 2. Document D1 discloses a method for detecting the clock frequency of a CPU clock. The CPU clock (system clock) is delivered to both the CPU (host) and a peripheral device. The frequency is detected by using a second, that is to say, secondary clock, whose clock frequency is known.
- 3. The subject matter of claim 1 essentially differs from document D1 in that in the system disclosed in document D1 the secondary clock signal belongs to the peripheral device and is not applied to the host. The subject matter of claim 1 is therefore novel (PCT Article 33(2)).
- 4. The problem to be solved by the present invention is therefore understood to be that of determining how to dispense with a quartz clock. If the host itself also has to carry out a method for determining the clock frequency of the system clock, an external



clock or an internal quartz clock becomes necessary.

5. The solution to this problem proposed in claim 1 is not known from the prior art or the general technical knowledge in the field and hence not obvious to a person skilled in the art. The re-use of the secondary clock would make little sense in document D1 since the peripheral device is designed as insulation between the system bus signals and the ISA bus signals. Consequently, the application meets the requirements for inventive step of PCT Article 33(3).

Claims 2-11 are dependent on claim 1 and therefore likewise meet the PCT requirements for novelty and inventive step.